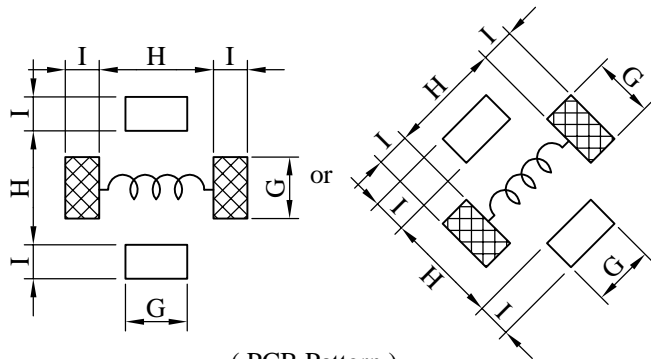
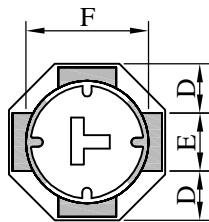
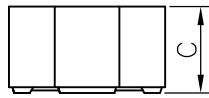
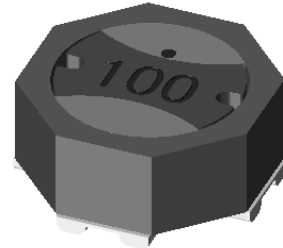
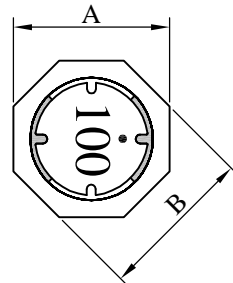


SPECIFICATION FOR APPROVAL

REF. :

PROD. NAME	Shielded SMD Power Inductor	ABC'S DWG NO.	SU8040□□□□F□-□□□		
		REV.	20150716-D	PAGE	1

I . Configuration and dimensions :



(PCB Pattern)

Unit : m/m

A	B	C	D	E	F	G	H	I
8.00 ±0.3	8.00 ±0.3	3.80 ±0.3	2.40 typ.	3.20 typ.	6.40 typ.	3.40 ref.	6.20 ref.	1.40 ref.

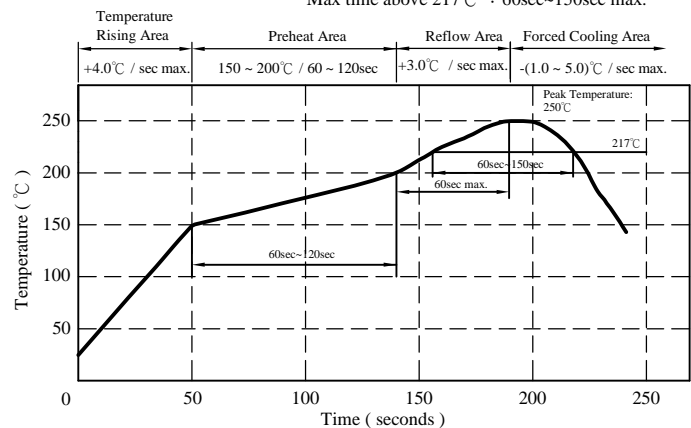
II . Description :

- a . Ferrite drum core construction.
- b . Magnetically shielded.
- c . Enamelled copper wire : F 、 H class
- d . Product weight : 0.82g (ref.)
- e . Moisture sensitivity Level 1
- f . Products comply with RoHS' requirements
- g . Halogen free available

Peak Temp : 250°C max.
Max. Peak Temp - 5°C : 30sec max.
Max time above 217°C : 60sec~150sec max.

III . General specification :

- a . Storage temp. : -40°C ----+125°C
- b . Operating temp. : -40°C ----+125°C
(Temp. rise included.)
- c . Resistance to solder heat : 250°C .10 secs.



AR-001C

SPECIFICATION FOR APPROVAL

REF. :

PROD. NAME	Shielded SMD Power Inductor	ABC'S DWG NO.	SU8040□□□□F□-□□□		
		REV.	20150716-D	PAGE	2

IV . Electrical characteristics :

DWG No.	Inductance (μH)	Q ref.	Test Freq.		SRF (MHz) typ.	RDC (mΩ)		Irms (A) typ	Isat (A) typ
			L(kHz)	Q(MHz)		typ.	max.		
SU80403R3YF□-□□□	3.3±30%	12	100	7.96	40.0	13.8	18.0	6.00	5.00
SU80404R2YF□-□□□	4.2±30%	12	100	7.96	32.0	16.5	22.0	5.30	4.60
SU80406R2YF□-□□□	6.2±30%	10	100	7.96	28.0	25.0	32.0	4.20	4.00
SU8040100YF□-□□□	10.0±30%	22	100	2.52	20.0	33.0	42.0	3.70	2.90
SU8040150YF□-□□□	15.0±30%	20	100	2.52	18.0	55.0	70.0	2.80	2.50
SU8040220YF□-□□□	22.0±30%	22	100	2.52	15.0	88.0	110.0	2.20	2.05
SU8040330YF□-□□□	33.0±30%	22	100	2.52	12.0	115.0	150.0	1.90	1.75
SU8040470YF□-□□□	47.0±30%	20	100	2.52	10.0	150.0	190.0	1.55	1.45
SU8040680YF□-□□□	68.0±30%	18	100	2.52	8.0	205.0	260.0	1.35	1.10
SU8040101YF□-□□□	100.0±30%	25	100	0.796	6.0	325.0	410.0	1.05	0.92
SU8040151YF□-□□□	150.0±30%	18	100	0.796	5.0	445.0	560.0	0.90	0.77

- 1). □: Packaging information : □ Code
- 2). "-□□□" : Reference code
- 3). Electrical specifications at 25°C
- 4). Inductance Test Freq. : 100kHz / 0.1V
- 5). Isat base on $\Delta L / L0A=35\%$ typ.
- 6). Irms base on Temp. rise 40°C typ.

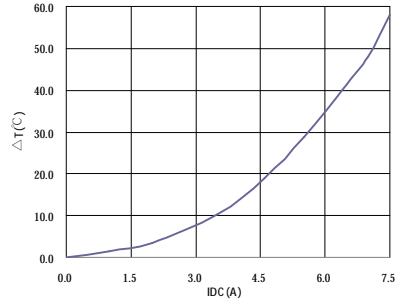
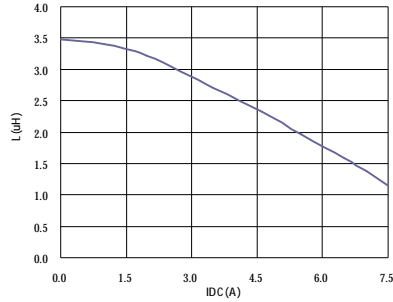
SPECIFICATION FOR APPROVAL

REF. :

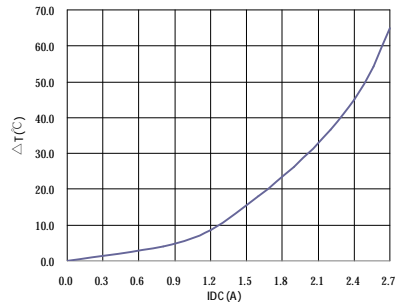
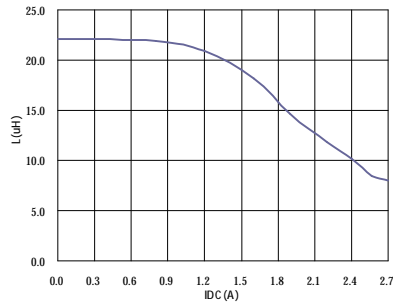
PROD. NAME	Shielded SMD Power Inductor	ABC'S DWG NO.	SU8040□□□□F□-□□□		
		REV.	20150716-D	PAGE	3

V. Curve :

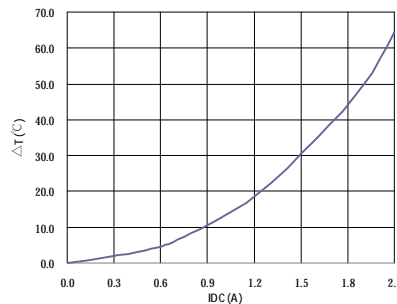
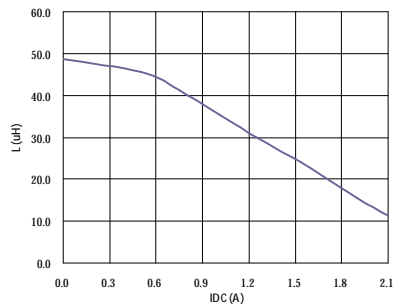
SU80403R3YF□



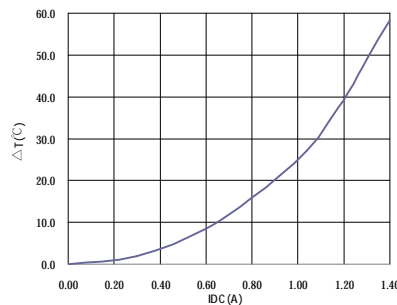
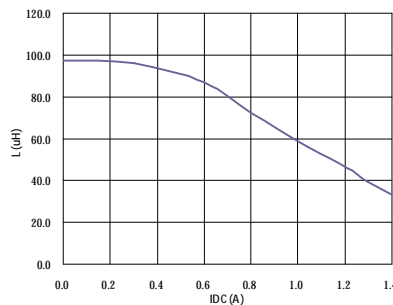
SU8040220YF□



SU8040470YF□



SU8040101YF□



AR-001C

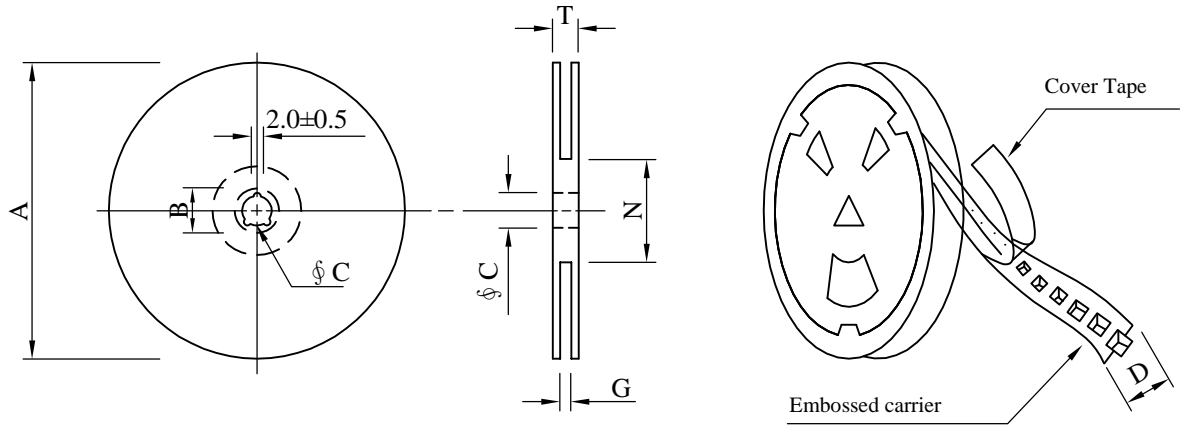
SPECIFICATION FOR APPROVAL

REF. :

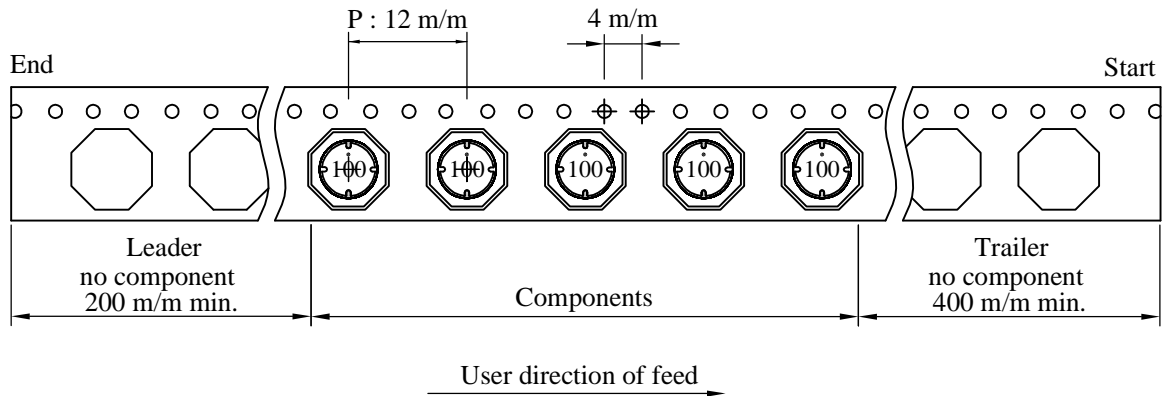
PROD. NAME	Shielded SMD Power Inductor	ABC'S DWG NO.	SU8040□□□□F□-□□□		
		REV.	20150716-D	PAGE	4

VI . Packaging information :

(1) Configuration



※Carrier tape width : D



(2) Dimensions

Unit:m/m

Style	A	B	C	D	G	N	T
13 - 16	330	21±0.8	13±0.5	16	18 ⁺⁰	50 ⁻⁰	22.4

(3) Q'TY & G.W. Per package

Code	Inner : Reel			Outer : Carton		
	Q'TY (pcs)	G.W. (gw)	Style	Q'TY (pcs)	G.W. (Kg)	Size (cm)
B	1,200	1420	13 - 16	7,200	9.8	38 x 37 x 22

AR-001C

SPECIFICATION FOR APPROVAL

REF. :

PROD. NAME	Shielded SMD Power Inductor	ABC'S DWG NO.	SU8040□□□□F□-□□□		
		REV.	20150716-D	PAGE	5

VII . Reliability test :

Item	Reference documents	Test Condition	Test Specification
1.High Temperature Exposure	MIL-STD-202 Method 108	1.Temperature: 125±2℃ 2.Time:96±2 hours.	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±20%.
2.Temperature Cycling	JESD22-A 104	1.Temperature: -40℃ ~ +125℃ 2.Number of cycle:100 cycle 3.Dwell time:30 minutes	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±20%.
3.Biased Humidity Test	MIL-STD-202 Method 103	1.Temperature : 85±2 ℃ 2.Humidity: 85% RH. 3.Time:96±2 Hours	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±20%.
4.Operational Life	JESD22-A 108	1.Temperature: 125℃ (Temp. rise included) 2.Time:96±2 hours. 3.Rated current	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±20%.
5.External Visual	JESD22-B 101 & MIL-STD-883 Method 2009	Inspect product constructions, marking and workmanship.	1.No pollution on the surface of products. 2.Clear marking. 3.No crack.
6.Physical Dimensions	JESD22-B 100	Verify physical dimensions to the applicable product detail specification.	Per product specification standard
7.Resistance to solvents	MIL-STD-202 Method 215	Immerse into solvent for 3±0.5 minutes & brush 10 times for 3 cycles.	1.No body change in apperarence. 2.No marking blurred. 3.Inductance shall not change more than ±20%.
8.Vibration Test	MIL-STD-202 Method 204	1.Frequency and Amplitued : 10-2000-10 Hz, 1.5 mm. 2.Direction:X, Y, Z 3.Test duration:2 hours for each direction, 6 hours in total.	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±20%.
9.Resistance To Soldering Heat Test	MIL-STD-202 Method 210 & J-STD020D.1	1.Highest temperature : 250±5℃. 2.Time (temp. ≥ 217℃) : 60~150 Second. 3.IR reflow times : 3 times.	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±20%.
10.Saturation Current	JIS C 6436 & User SPEC.	1.Applied rated current for 5 second. 2.Saturation current	Inductance shall not drop more than 35% typ.
11.Over load	JIS C 6436 & User SPEC.	1.Applied one and half rated current for a period of 5 minutes. 2.Rated current	No electrical or mechanical damage
12.Temperature Rise Current	JIS C 6436 & User SPEC.	1.Applied rated current for 10 minutes. 2.Temperature measure by digital surface thermometer. 3.Irms current	Surface temperature rise is less than 40℃ typ.
13.Solderability Test	J-STD-002 & JESD22-B 102	1.Baking in pre-testing : 150±5℃ / 16Hours±30 min. 2.Peak temperature : 240±5℃ 3.Time (temp. ≥ 217℃) : 60~150 second. 4.IR reflow times : 1 times.	More than 95% soldering coverage min on terminations.
14.Electrical Characteriazation	MIL-STD-202 Method 304 & User SPEC.	1.Operating temperature : -40℃~125℃ 2.Room temperature : 25℃.	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±20%.
15.Drop	CNS-C6354 & GB/T 2423.8	1.Products shall be mounted on SPEC. PCB and dropped down from a height of 1m 2.Drop total time : 6 time (Every side of sample drop 2 time)	1. Adhesion on PCB shall be enough. 2. Product appearance shall not break. 3. No electrical damage.
16.Terminal Strength Test	IEC 60068-2-21	1.Apply push force to samples mounted on PCB. 2.Force of 1.8 kg for 60±1 seconds.	After test, inductors shall be no mechanical damage.

AR-001C