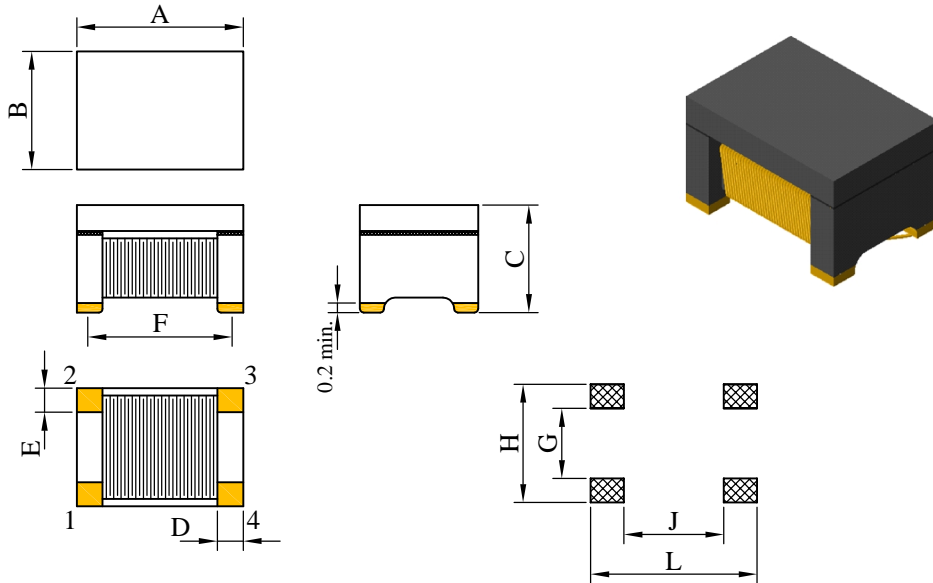


SPECIFICATION FOR APPROVAL

REF. :

PROD. NAME	SMD Common Mode Filter	ABC'S DWG NO.		SF4532□□□□F□-□□□	
		REV.	20161004-G	PAGE	1

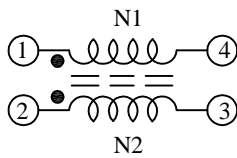
I . Configuration and dimensions :



Unit : m/m

A	B	C	D	E	F	G	H	J	L
4.50 ±0.2	3.20 ±0.2	3.00 max.	0.70 ref.	0.65 ref.	3.80 ref.	1.60 ref.	3.40 ref.	3.20 ref.	5.90 ref.

II . Schematic diagram :



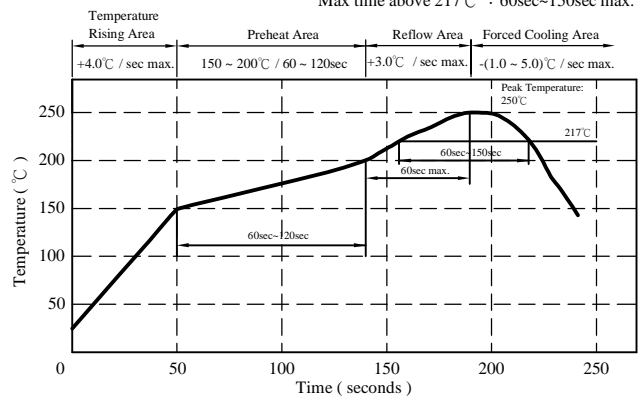
III . Description :

- a . Ferrite drum core construction.
- b . Magnetically shielded.
- c . Enamelled copper wire : H class
- d . Product weight : 0.15 g (ref.)
- e . Moisture sensitivity Level 1
- f . Products comply with RoHS' requirements
- g . Halogen Free available

IV . General specification :

- a . Storage temp. : -40°C ----+125°C
- b . Operating temp. : -40°C ----+125°C
(Temp. rise included)
- c . Resistance to solder heat : 250°C .10 secs.

Peak Temp : 250°C max.
Max. Peak Temp - 5°C : 30sec max.
Max time above 217°C : 60sec~150sec max.



AR-001C

SPECIFICATION FOR APPROVAL

REF. :

PROD. NAME	SMD Common Mode Filter	ABC'S DWG NO.	SF4532□□□□F□-□□□		
		REV.	20161004-G	PAGE	2

V . Electrical characteristics :

DWG No.	Inductance (μ H)	Lstray (μ H) typ.	RDC (Ω) max.	IDC (A) max.	Common mode impedance (K Ω)(@10MHz)	
					min.	typ.
SF4532110YFB-□□□	11.0 ^{+50%} _{-30%}	0.10	0.50	0.36	0.30	0.60
SF4532220YFB-□□□	22.0 ^{+50%} _{-30%}	0.15	0.60	0.31	0.60	1.20
SF4532510YFB-□□□	51.0 ^{+50%} _{-30%}	0.25	1.00	0.23	1.50	3.50
SF4532101YFB-□□□	100.0 ^{+50%} _{-30%}	0.30	1.50	0.20	3.00	7.50
SF4532201YFB-□□□	200.0 ^{+50%} _{-30%}	0.45	4.50	0.10	5.00	11.00

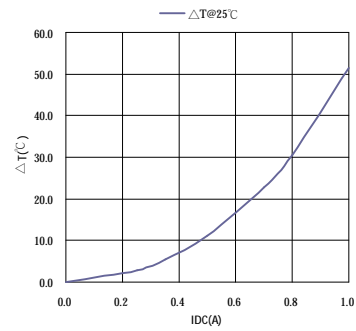
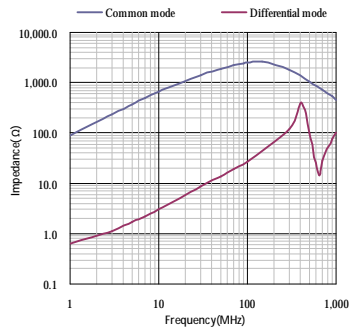
- 1). □: Packaging information : □ Code
- 2). "-□□□" : Reference code
- 3). Electrical specifications at 25°C
- 4). Inductance Test Condition. : 100kHz / 0.1V
- 5). IDC base on Temp. rise 40°C max.
- 6). Insulation resistance : 10M Ω min.
- 7). Rated voltage : 50Vdc

SPECIFICATION FOR APPROVAL

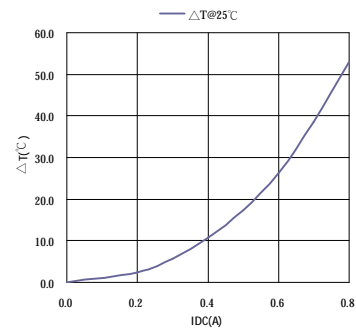
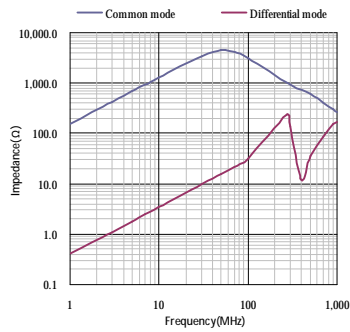
REF. :

PROD. NAME	SMD Common Mode Filter	ABC'S DWG NO.	SF4532□□□□F□-□□□		
		REV.	20161004-G	PAGE	3

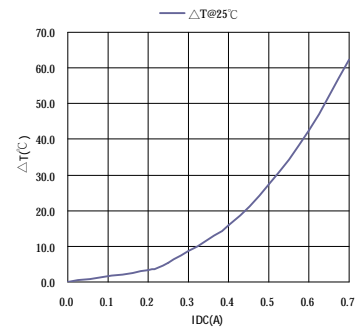
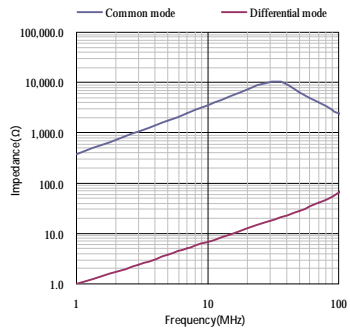
VI . Curve :
SF4532110YFB



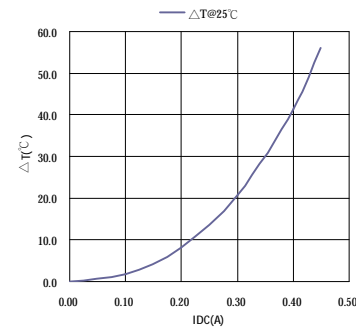
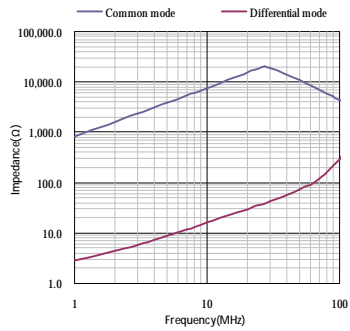
SF4532220YFB



SF4532510YFB



SF4532101YFB



AR-001C

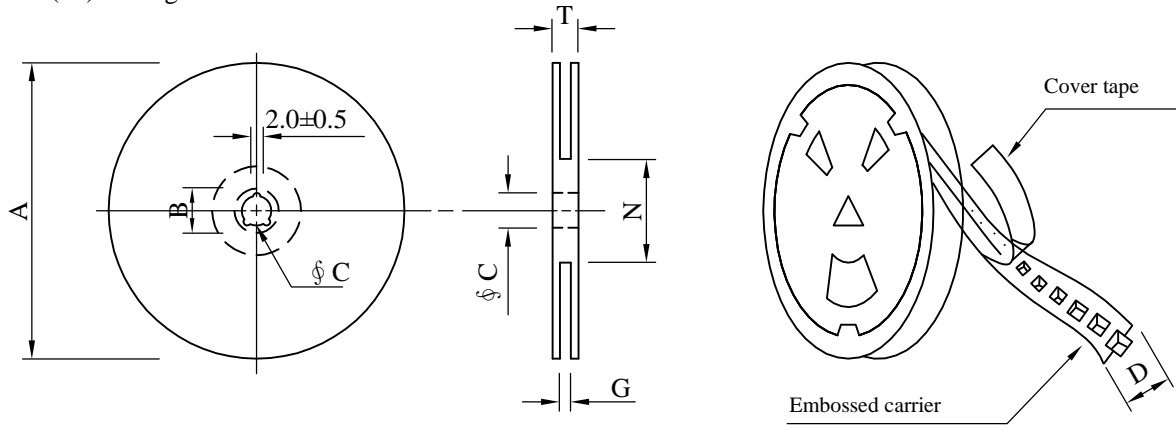
SPECIFICATION FOR APPROVAL

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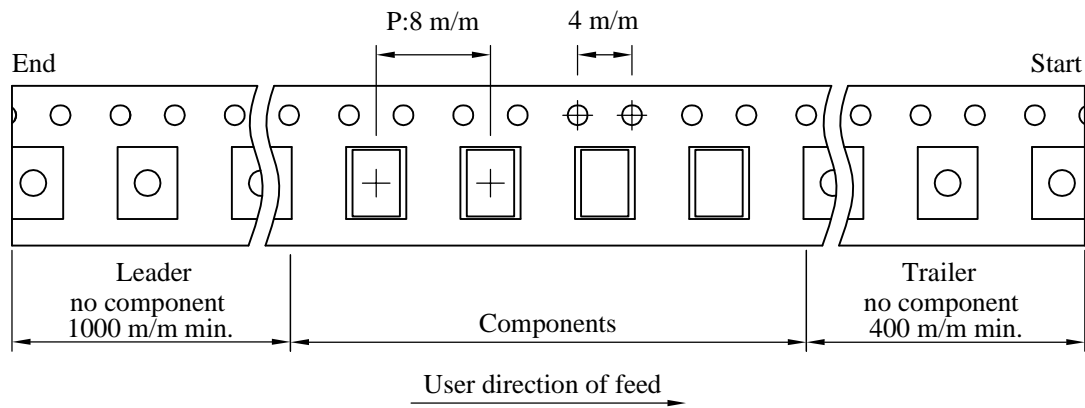
PROD. NAME	SMD Common Mode Filter	ABC'S DWG NO.		SF4532□□□□F□-□□□	
		REV.	20161004-G	PAGE	4

VII . Packaging information :

(1) Configuration



※Carrier tape width : D



(2) Dimensions

Unit:m/m

Style	A	B	C	D	G	N	T
07 - 12	178	21±0.8	13	12	14 ⁺⁰	50 ⁻⁰	16.5

(3) Q'TY & G.W. Pe package

Code	Inner : Reel			Outer : Carton		
	Q'TY (pcs)	G.W. (gw)	Style	Q'TY (pcs)	G.W. (Kg)	Size (cm)
B	500	120	07 - 12	20,000	4.80	41 x 39 x 22

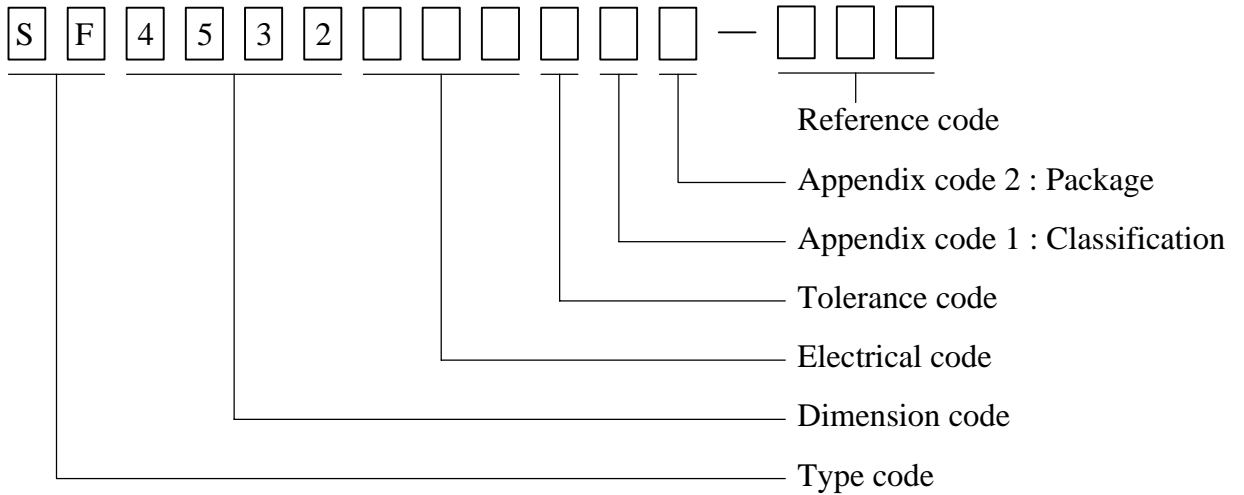
AR-001C

SPECIFICATION FOR APPROVAL

REF. :

PROD. NAME	SMD Common Mode Filter	ABC'S DWG NO.	SF4532□□□□F□-□□□		
		REV.	20161004-G	PAGE	5

VIII . Drawing number expression :



Appendix code 1 : Product Classification

Appendix code 2 : Package Information

Code	Inner package	Cover tape	Carrier tape	Bag	Package QTY	Remark
B	T /R (Reel package)	Heat seal	Non-antistatic	Antistatic	500 pcs	

AR-001C

SPECIFICATION FOR APPROVAL

REF. :

PROD. NAME	SMD Common Mode Filter	ABC'S DWG NO.	SF4532□□□□F□-□□□		
		REV.	20161004-G	PAGE	6

IX . Reliability test :

Item	Reference documents	Test Condition	Test Specification
1.High Temperature Exposure	MIL-STD-202 Method 108	1.Temperature: 125±2℃ 2.Time:96±2 hours.	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±50%.
2.Temperature Cycling	JESD22-A 104	1.Temperature: -40℃ ~ +125℃ 2.Number of cycle:100 cycle 3.Dwell time:30 minutes	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±50%.
3.Biased Humidity Test	MIL-STD-202 Method 103	1.Temperature : 85±2 ℃ 2.Humidity: 85% RH. 3.Time:96±2 Hours	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±50%.
4.Operational Life	JESD22-A 108	1.Temperature: 125℃ (Temp. rise included) 2.Time:96±2 hours. 3.Rated current	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±50%.
5.External Visual	JESD22-B 101 & MIL-STD-883 Method 2009	Inspect product constructions, marking and workmanship.	1.No pollution on the surface of products. 2.Clear marking. 3.No crack.
6.Physical Dimensions	JESD22-B 100	Verify physical dimensions to the applicable product detail specification.	Per product specification standard
7.Resistance to solvents	MIL-STD-202 Method 215	Immerse into solvent for 3±0.5 minutes & brush 10 times for 3 cycles.	1.No body change in appearance. 2.No marking blurred. 3.Inductance shall not change more than ±50%.
8.Vibration Test	MIL-STD-202 Method 204	1.Frequency and Amplitud : 10-2000-10 Hz, 1.5 mm. 2.Direction:X, Y, Z 3.Test duration:2 hours for each direction, 6 hours in total.	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±50%.
9.Resistance To Soldering Heat Test	MIL-STD-202 Method 210 & J-STD020D.1	1.Highest temperature : 250±5℃. 2.Time (temp. ≥ 217℃) : 60~150 Second. 3.IR reflow times : 3 times.	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±50%.
10.Over load	JIS C 6436 & User SPEC.	1.Applied one and half rated current for a period of 5 minutes. 2.Rated current	No electrical or mechanical damage
11.Temperature Rise Current	JIS C 6436 & User SPEC.	1.Applied rated current for 10 minutes. 2.Temperature measure by digital surface thermometer. 3.Irms current	Surface temperature rise is less than 40℃ max.
12.Solderability Test	J-STD-002 & JESD22-B 102	1.Baking in pre-testing : 150±5℃ / 16Hours±30 min. 2.Peak temperature : 240±5℃ 3.Time (temp. ≥ 217℃) : 60~150 second. 4.IR reflow times : 1 times.	More than 95% soldering coverage min on terminations.
13.Electrical Characteriazation	MIL-STD-202 Method 304 & User SPEC.	1.Operating temperature : -40℃~125℃ 2.Room temperature : 25℃.	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±50%.
14.Withstanding Voltage Test	MIL-STD-202 Method 301 & User SPEC.	1.DC: 250 V (Winding to Winding) 2.Time : 2 sec.	1.During the test no breakdown. 2.No mechanical or electrical damage.
15.Drop	CNS-C6354 & GB/T 2423.8	1.Products shall be mounted on SPEC. PCB and dropped down from a height of 1m 2.Drop total time : 6 time (Every side of sample drop 2 time)	1. Adhesion on PCB shall be enough. 2. Product appearance shall not break. 3. No electrical damage.
16.Terminal Strength Test	IEC 60068-2-21	1.Apply push force to samples mounted on PCB. 2.Force of 1.8 kg for 60±1 seconds.	After test, inductors shall be no mechanical damage.

AR-001C

SPECIFICATION FOR APPROVAL

REF. :

PROD. NAME	SMD Common Mode Filter	ABC'S DWG NO.	SF4532□□□□F□-□□□		
		REV.	20161004-G	PAGE	7

X . Change history :

DATE/REV.	DISCRIPTION	DRAWN	CHECKED	APPROVED
20140311-A	Released	Miz Hsieh	Leo Liang	Nick Chen
20140710-B	Add Inductance 201Y			
20140806-C	Add terminal height dimensions 0.2mm min.			
20150714-D	1. Modify the the 101Y RDC(Ω)max. : 2.00→1.50 2. Add the Electrical characteristics : a. Lstray b. Rated Voltage : 50Vdc c. Insulation resistance 10M Ω min. 3. Modify the Reliability test			
20150731-E	Modify the 101Y-Lstray(typ.) : from 0.35uH→ 0.30uH			
20160630-F	Modify Packaging : Leader direction from 200 m/m → 1000 m/m min.			
20161004-G	1. Add Change history and Drawing number expression 2. Modify the package carrier tape 2D drawing			

AR-001C