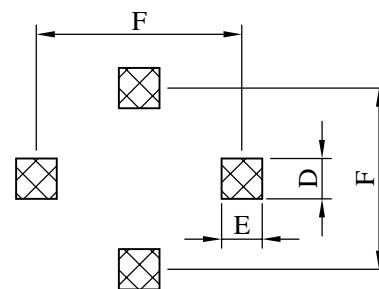
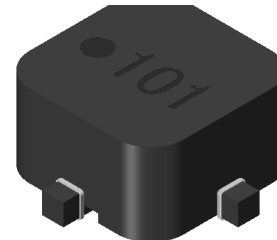
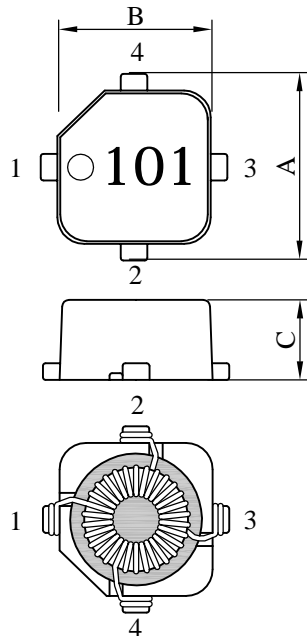


SPECIFICATION FOR APPROVAL

REF. :

PROD. NAME	SMD Power Inductor	ABC'S DWG NO.	SF1206□□□□L□-□□□		
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I . Configuration and dimensions :

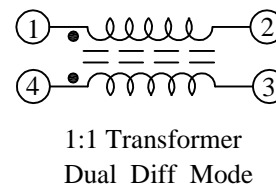
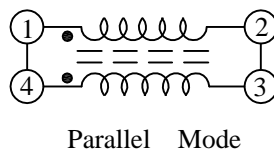
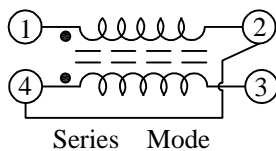


(PCB Pattern)

Unit : m/m

A	B	C	D	E	F
11.50 ±0.5	9.00 ±0.5	5.70 max.	2.54 typ.	2.54 typ.	9.80 typ.

II . Schematic diagram :



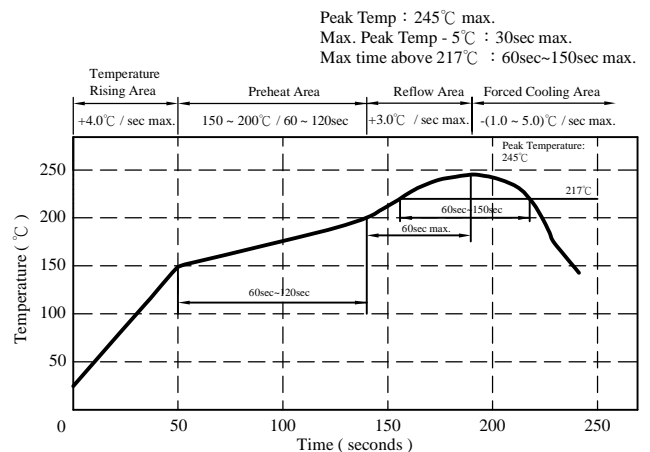
" ● " : Polarity

III . Description :

- a . Iron toroidal core construction.
- b . Enamelled copper wire : H class
- c . Product weight : 1.00g (ref.)
- d . Moisture sensitivity Level 1
- e . Products comply with RoHS' requirements
- f . Halogen free available

IV . General specification :

- a . Storage temp. : -55°C ----+125°C
- b . Operating temp. : -55°C ----+125°C
(Temp. rise included)
- c . Resistance to solder heat : 245°C .10 secs.



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V . Electrical characteristics :

Dwg. No.	Parallel				Series			
	L (uH) 1K/0.1V	Irms (A)	L (uH) @rated I	RDC (Ω) MAX.	L (uH) 1K/0.1V	Irms (A)	L (uH) @rated I	RDC (Ω) MAX.
SF12065R0ML□-□□□	5 ±20%	2.41	3.8 ±20%	0.023	20 ±20%	1.20	15.2 ±20%	0.092
SF12068R0ML□-□□□	8 ±20%	1.90	6.1 ±20%	0.037	32 ±20%	0.95	23.2 ±20%	0.150
SF1206100ML□-□□□	10 ±20%	1.83	7.4 ±20%	0.040	40 ±20%	0.92	29.6 ±20%	0.160
SF1206150ML□-□□□	15 ±20%	1.45	11.1 ±20%	0.063	60 ±20%	0.73	44.4 ±20%	0.250
SF1206200ML□-□□□	20 ±20%	1.25	15.2 ±20%	0.086	80 ±20%	0.63	60.8 ±20%	0.340
SF1206250ML□-□□□	25 ±20%	1.17	18.5 ±20%	0.098	100 ±20%	0.59	74.0 ±20%	0.390
SF1206330ML□-□□□	33 ±20%	0.98	24.8 ±20%	0.140	132 ±20%	0.49	99.2 ±20%	0.560
SF1206500ML□-□□□	50 ±20%	0.78	37.5 ±20%	0.220	200 ±20%	0.38	150.0 ±20%	0.880
SF1206680ML□-□□□	68 ±20%	0.72	49.6 ±20%	0.260	272 ±20%	0.36	198.4 ±20%	1.040
SF1206101ML□-□□□	100 ±20%	0.58	74.0 ±20%	0.400	400 ±20%	0.29	296.0 ±20%	1.600
SF1206151ML□-□□□	150 ±20%	0.47	111.0 ±20%	0.600	600 ±20%	0.24	444.0 ±20%	2.400
SF1206201ML□-□□□	200 ±20%	0.40	150.0 ±20%	0.840	800 ±20%	0.20	600.0 ±20%	3.360
SF1206251ML□-□□□	250 ±20%	0.33	192.5 ±20%	1.190	1000 ±20%	0.17	770.0 ±20%	4.760
SF1206301ML□-□□□	300 ±20%	0.32	225.0 ±20%	1.310	1200 ±20%	0.16	900.0 ±20%	5.240

1). □ : Packaging information : □ Code

2)."-□□□":Reference code

3). Irms : Base on temp. rise 35°C max.

Note : SF1206500M parallel inductance may change to 51.18uH @ 10KHz/1V.

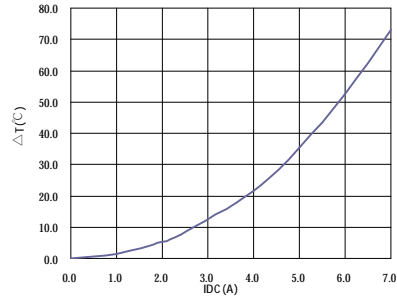
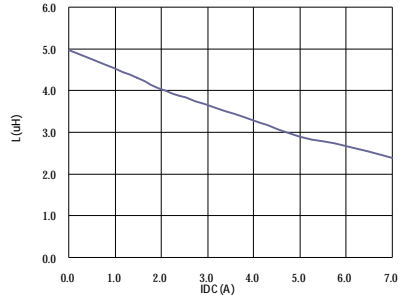
SPECIFICATION FOR APPROVAL

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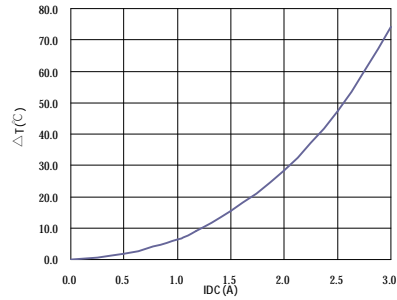
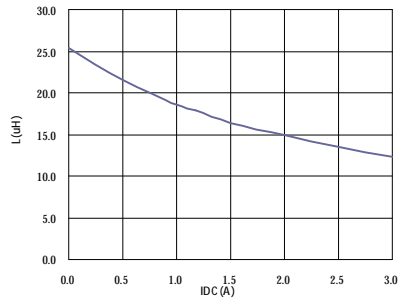
PROD. NAME	SMD Power Inductor	ABC'S DWG NO.	SF1206□□□□L□-□□□		
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V . Curve :

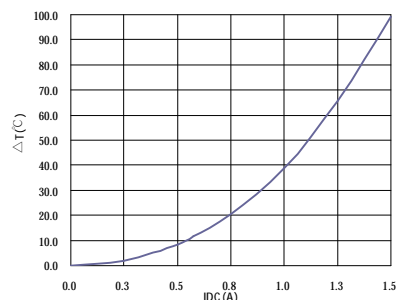
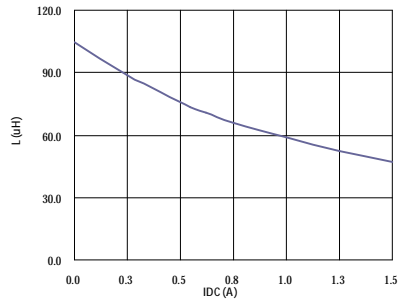
SF12065R0ML□



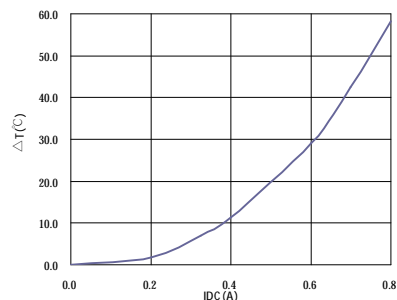
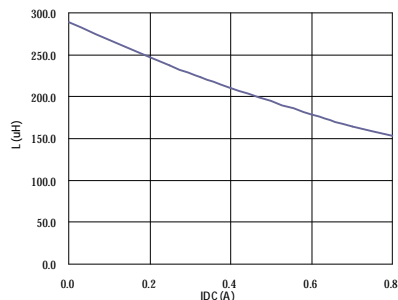
SF1206250ML□



SF1206101ML□



SF1206301ML□



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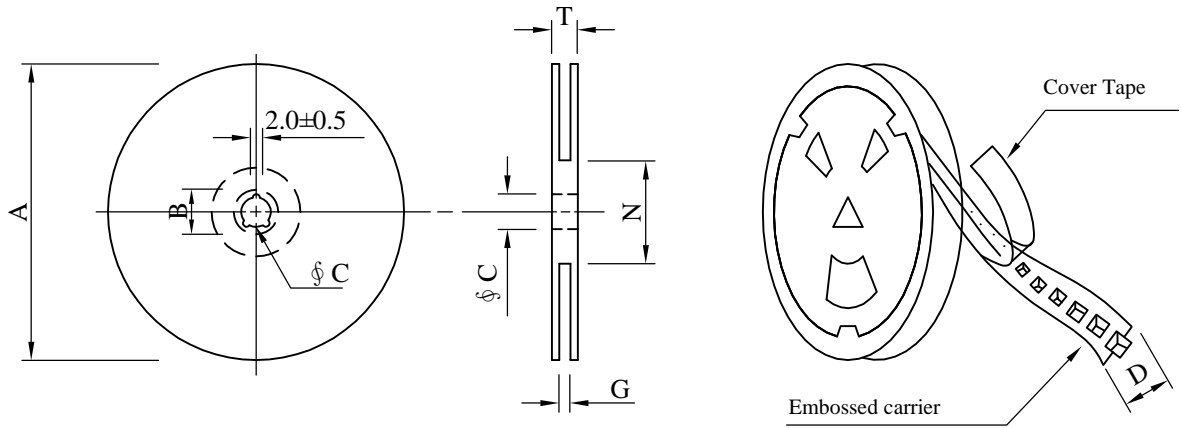
SPECIFICATION FOR APPROVAL

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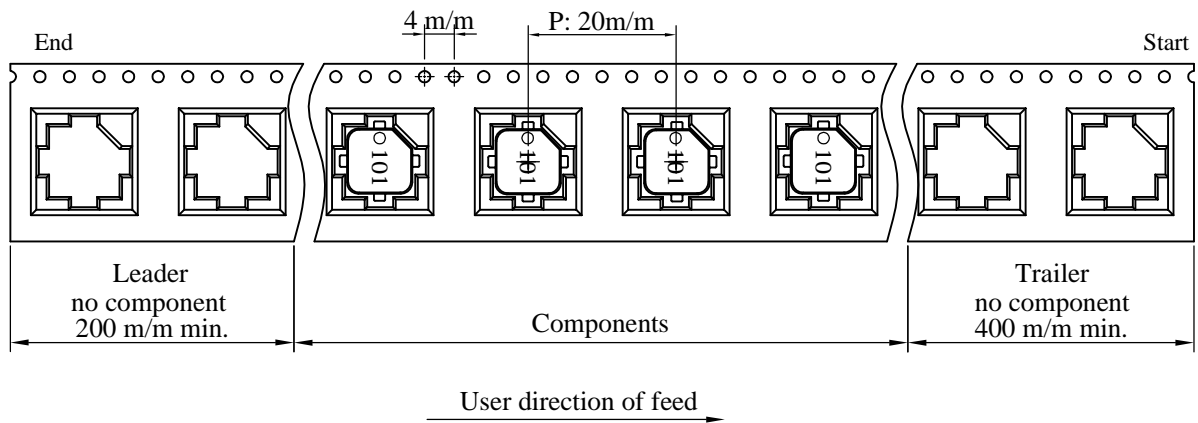
PROD. NAME	SMD Power Inductor	ABC'S DWG NO.	SF1206□□□□L□-□□□		
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VI . Packaging information :

(1) Configuration



※Carrier tape width : D



(2) Dimensions : (m/m)

Style	A	B	C	D	G	N	T
13 - 24	330	21±0.8	13±0.5	24	26 ⁺⁰	60 ⁻⁰	30.4

(3) Q'TY & G.W. Per package :

Code	Inner : Reel			Outer : Carton		
	Q'TY (pcs)	G.W. (gw)	Style	Q'TY (pcs)	G.W. (kg)	Size (cm)
B	400	800	13 - 24	1600	4.0	38 x 37 x 22

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SPECIFICATION FOR APPROVAL

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VII . Reliability test :

Item	Reference documents	Test Condition	Test Specification
1.High Temperature Exposure	MIL-STD-202 Method 108	1.Temperature: 125°C 2.Time:96 hours.	1.No mechanical and electrical damage. 2.Inductance shall not change more than ±50%.
2.Temperature Cycling	JESD22 Method JA-104	1.Temperature: -55°C ~ 125°C 2.Number of cycle:96 cycle 3.Dwell time:30 minutes	1.No mechanical and electrical damage. 2.Inductance shall not change more than ±50%.
3.Biased Humidity Test	MIL-STD-202 Method 103	1.Temperature:85±5 °C 2.Time:96 Hours 3.Humidity: 85±5% RH.	1.No mechanical and electrical damage. 2.Inductance shall not change more than ±50%.
4.Operational Life	MIL-PRF-27	1.Temperature: 125°C 2.Time:96 hours. 3.Apply rated current.	1.No mechanical and electrical damage. 2.Inductance shall not change more than ±50%.
5.External Visual	MIL-STD-883 Method 2009	Inspect product constructions, marking and workmanship.	1.No pollution on the surface of products. 2.Clear marking. 3.No crack.
6.Physical Dimensions	JESD22 Method JB-100	Verify physical dimensions to the applicable product detail specification.	Per product specification standard
7.Resistance to solvents	MIL-STD-202 Method 215	Immerse into solvent for 3±0.5 minutes & brush 10 times for 3 cycles.	1.No body change in apperarence. 2.No marking blurred. 3.Inductance shall not change more than ±50%.
8.Vibration Test	MIL-STD-202 Method 204	1.Frequency and Amplitued : 10-2000-10 Hz, 1.5 mm. 2.Direction:X, Y, Z 3.Test duration:2 hours for each direction, 6 hours in total.	1.No mechanical and electrical damage. 2.Inductance shall not change more than ±50%.
9.Resistance To Soldering Heat Test	MIL-STD-202 Method 210	1.Highest temperature : 245±5°C 2.Time (temp. ≥ 217°C) : 60~150 Second. 3.IR reflow times : 3 times.	1.No mechanical and electrical damage. 2.Inductance shall not change more than ±50%.
10.Rated current	MIL-STD-202 Method 330	Apply rated current for 5 second.	1.No mechanical and electrical damage. 2.Inductance shall not change more than ±50%.
11.Temperature rise	MIL-PRF-27	Apply rated current for 10 minutes.	1.No mechanical and electrical damage. 2.Inductance shall not change more than ±50%.
12.Over load	MIL-PRF-27	Apply double as rated current for 5 minutes. (It's not application to some special design)	1.No mechanical and electrical damage. 2.Inductance shall not change more than ±50%.
13.Solderability Test	J-STD-002	1.Baking in pre-testing : 155±5°C / 16Hours±30 min. 2.Peak temperature : 240±5°C 3.Time (temp. ≥ 217°C) : 60~150 second. 4.IR reflow times : 1 times.	The terminal shall be at least 95% covered with fresh solder.
14.Electrical Characteriazation	User Spec.	1.Operating temperature : -55°C~125°C 2.Room temperature : 25°C.	1.No mechanical and electrical damage. 2.Inductance shall not change more than ±50%.
15.Withstanding Voltage Test	MIL-STD-202 Method 201	1.DC:500V 2.Time:1minutes	1.During the test no breakdown. 2.The characteristic is normal after test.
16.Drop	JESD22-B111	Packaged & Drop down from 1m.In 1 angle 1ridges & 2 surfaces orientation.	1.No case deformation or change in appearance. 2.Inductance shall not change more than ±50%.
17.Terminal Strength Test	JIS-C-6429	1.Apply push force to samples mounted on PCB. 2.Force of 1.8 kg for 60±1 seconds.	After test, inductors shall be no mechanical damage.

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