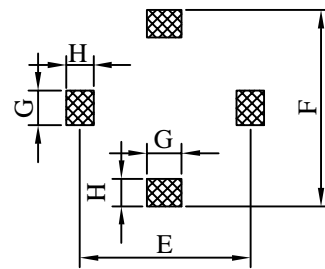
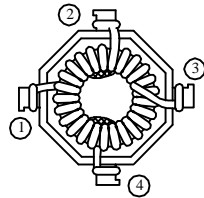
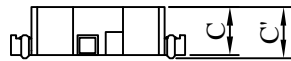
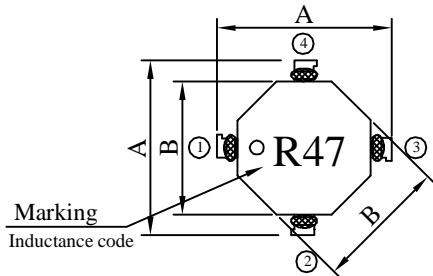


SPECIFICATION FOR APPROVAL

REF. :

PROD. NAME	SMD Power Inductor	ABC'S DWG NO.	SF1105□□□□L□-□□□		
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I . Configuration and dimensions :

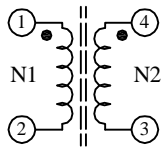


(PCB Pattern)

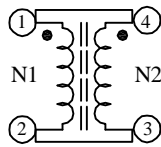
Unit : m/m

A	B	C	C'	E	F	G	H
11.50±0.3	8.80±0.3	4.850±0.3	5.20 max.	9.96 ref.	13.00 ref.	3.68 ref.	3.05 ref.

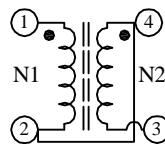
II . Schematic diagram :



Transformers



Parallel



Series

" ● " : Polarity

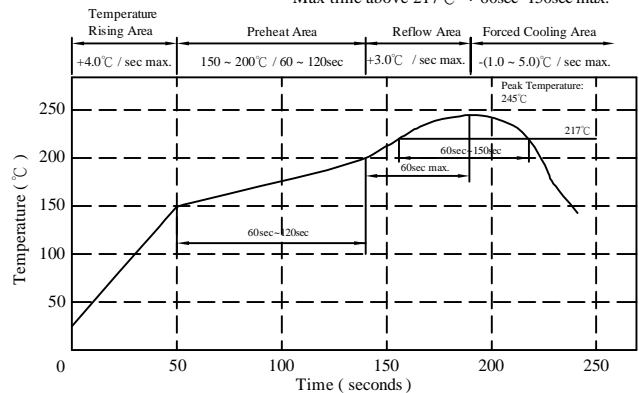
III . Description :

- a . Ferrite toroidal core construction.
- b . Enamelled copper wire : H class
- c . Product weight : 0.44g (ref.)
- d . Moisture sensitivity Level 1
- e . Products comply with RoHS' requirements
- f . Halogen free available

IV . General specification :

- a . Storage temp. : -40°C ----+105°C
- b . Operating ambient temp. : -40°C ----+105°C
(Temp. rise included)
- c . Resistance to solder heat : 245°C. 10 secs.

Peak Temp : 245°C max.
Max. Peak Temp - 5°C : 30sec max.
Max time above 217°C : 60sec~150sec max.



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SPECIFICATION FOR APPROVAL

REF. :

PROD. NAME	SMD Power Inductor	ABC'S DWG NO.	SF1105□□□□L□-□□□		
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V . Electrical characteristics :

DWG No.	Open Circuit Inductance (μ H)		Full Load Current (A) max.		DC Resistance (m Ω) max	
	PARALLEL	SERIES	PARALLEL	SERIES	PARALLEL	SERIES
SF1105R47YL□-□□□	0.47 \pm 30%	1.88 \pm 30%	5.90	2.95	12	48
SF1105R68YL□-□□□	0.68 \pm 30%	2.72 \pm 30%	5.40	2.70	14	56
SF11051R0ML□-□□□	1.00 \pm 20%	4.00 \pm 20%	5.00	2.50	16	64
SF11052R0ML□-□□□	2.00 \pm 20%	8.00 \pm 20%	3.90	1.95	25	100
SF11055R0ML□-□□□	5.00 \pm 20%	20.00 \pm 20%	2.50	1.25	40	160
SF11058R0ML□-□□□	8.00 \pm 20%	32.00 \pm 20%	2.30	1.15	60	240
SF1105100ML□-□□□	10.00 \pm 20%	40.00 \pm 20%	2.10	1.05	70	280
SF1105150ML□-□□□	15.00 \pm 20%	60.00 \pm 20%	1.60	0.80	85	340
SF1105200ML□-□□□	20.00 \pm 20%	80.00 \pm 20%	1.50	0.75	105	420
SF1105250ML□-□□□	25.00 \pm 20%	100.00 \pm 20%	1.40	0.70	120	480
SF1105330ML□-□□□	33.00 \pm 20%	132.00 \pm 20%	1.30	0.65	150	600
SF1105500ML□-□□□	50.00 \pm 20%	200.00 \pm 20%	0.82	0.41	310	1240
SF1105680ML□-□□□	68.00 \pm 20%	272.00 \pm 20%	0.76	0.38	365	1460
SF1105101ML□-□□□	100.00 \pm 20%	400.00 \pm 20%	0.62	0.31	545	2180
SF1105151ML□-□□□	150.00 \pm 20%	600.00 \pm 20%	0.56	0.28	665	2660
SF1105201ML□-□□□	200.00 \pm 20%	800.00 \pm 20%	0.46	0.23	955	3820
SF1105301ML□-□□□	300.00 \pm 20%	1200.00 \pm 20%	0.42	0.21	1400	5600

- 1). □: Packaging information : □ Code
- 2). "-□□□" : Reference code
- 3). Electrical specifications at 25°C
- 4). Irms base on Temp. rise 40°C max.
- 5). Isat base on Δ L/L0A=35% typ.
- 6). Inductance Test Condition. : 100KHz / 0.25V

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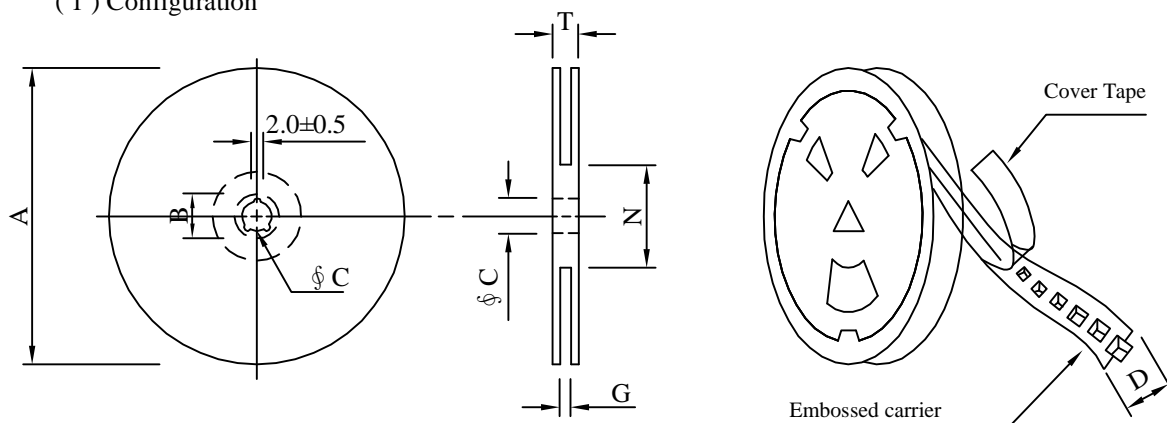
SPECIFICATION FOR APPROVAL

REF. :

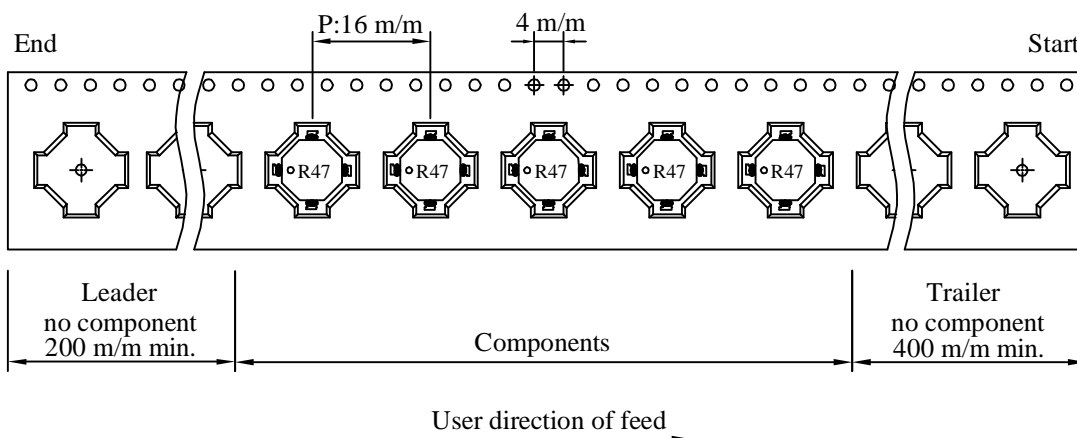
PROD. NAME	SMD Power Inductor	ABC'S DWG NO.	SF1105□□□□L□-□□□		
		REV.	20121011-A	PAGE	3

VI . Packaging information :

(1) Configuration



※Carrier tape width : D



(2) Dimensions

Unit:m/m

Style	A	B	C	D	G	N	T
13 - 24	330	21±0.8	13±0.5	24	26 ⁺⁰	60 ⁻⁰	30.4

(3) Q'TY & G.W. Per package

Code	Inner : Reel			Outer : Carton		
	Q'TY (pcs)	G.W. (gw)	Style	Q'TY (pcs)	G.W. (Kg)	Size (cm)
B	600	800	13 - 24	2,400	4.5	38 x 37 x 22

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SPECIFICATION FOR APPROVAL

REF. :

PROD. NAME	SMD Power Inductor	ABC'S DWG NO.	SF1105□□□□L□-□□□		
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VII . Reliability test :

Item	Reference documents	Test Condition	Test Specification
1.High Temperature Exposure	MIL-STD-202 Method 108	1.Temperature: 105°C 2.Time:96 hours.	1.No mechanical and electrical damage. 2.Inductance shall not change more than ±50%.
2.Temperature Cycling	JESD22 Method JA-104	1.Temperature: -40°C ~ 105°C 2.Number of cycle:96 cycle 3.Dwell time:30 minutes	1.No mechanical and electrical damage. 2.Inductance shall not change more than ±50%.
3.Biased Humidity Test	MIL-STD-202 Method 103	1.Temperature: 85±5 °C 2.Time:96 Hours 3.Humidity: 85±5% RH.	1.No mechanical and electrical damage. 2.Inductance shall not change more than ±50%.
4.Operational Life	MIL-PRF-27	1.Temperature: 105°C 2.Time:96 hours. 3.Apply rated current.	1.No mechanical and electrical damage. 2.Inductance shall not change more than ±50%.
5.External Visual	MIL-STD-883 Method 2009	Inspect product constructions, marking and workmanship.	1.No pollution on the surface of products. 2.Clear marking. 3.No crack.
6.Physical Dimensions	JESD22 Method JB-100	Verify physical dimensions to the applicable product detail specification.	Per product specification standard
7.Resistance to solvents	MIL-STD-202 Method 215	Immerse into solvent for 3±0.5 minutes & brush 10 times for their cycles.	1.No body change in appearance. 2.No marking blurred. 3.Inductance shall not change more than ±50%.
8.Vibration Test	MIL-STD-202 Method 204	1.Frequency and Amplitud : 10-2000-10 Hz, 1.5 mm. 2.Direction:X, Y, Z 3.Test duration:2 hours for each direction, 6 hours in total.	1.No mechanical and electrical damage. 2.Inductance shall not change more than ±50%.
9.Resistance To Soldering Heat Test	MIL-STD-202 Method 210	1.Highest temperature : 245±5°C 2.Time (temp. ≥ 217°C) : 60~150 Second. 3.IR reflow times : 3 times.	1.No mechanical and electrical damage. 2.Inductance shall not change more than ±50%.
10.Rated current	MIL-STD-202 Method 330	Apply rated current for 5 second.	1.No mechanical and electrical damage. 2.Inductance shall not change more than ±50%.
11.Temperature rise	MIL-PRF-27	Apply rated current for 10 minutes.	1.No mechanical and electrical damage. 2.Inductance shall not change more than ±50%.
12.Over load	MIL-PRF-27	Apply twice as rated current for 5 minutes. (It's not application to some special design)	1.No mechanical and electrical damage. 2.Inductance shall not change more than ±50%.
13.Solderability Test	J-STD-002	1.Baking in pre-testing : 155±5°C / 16Hours±30 min. 2.Peak temperature : 240±5°C 3.Time (temp. ≥ 217°C) : 60~150 second. 4.IR reflow times : 1 times.	The terminal shall be at least 95% covered with fresh solder.
14.Electrical Characteriazation	User Spec.	1.Operating temperature : -40°C ~105°C 2.Room temperature : 25°C.	1.No mechanical and electrical damage. 2.Inductance shall not change more than ±50%.
15.Withstanding Voltage Test	MIL-STD-202 Method 201	1.DV:500V 2.Time:1minutes	1.During the test no breakdown. 2.The characteristic is normal after test.
16.Drop	JESD22-B111	Packaged & Drop down from 1m.In 1 angle 1ridges & 2 surfaces orientation.	1.No case deformation or change in appearance. 2.Inductance shall not change more than ±50%.
17.Terminal Strength Test	JIS-C-6429	1.Apply push force to samples mounted on PCB. 2.Force of 1.8 kg for 60±1 seconds.	After test, inductors shall be no mechanical damage.

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