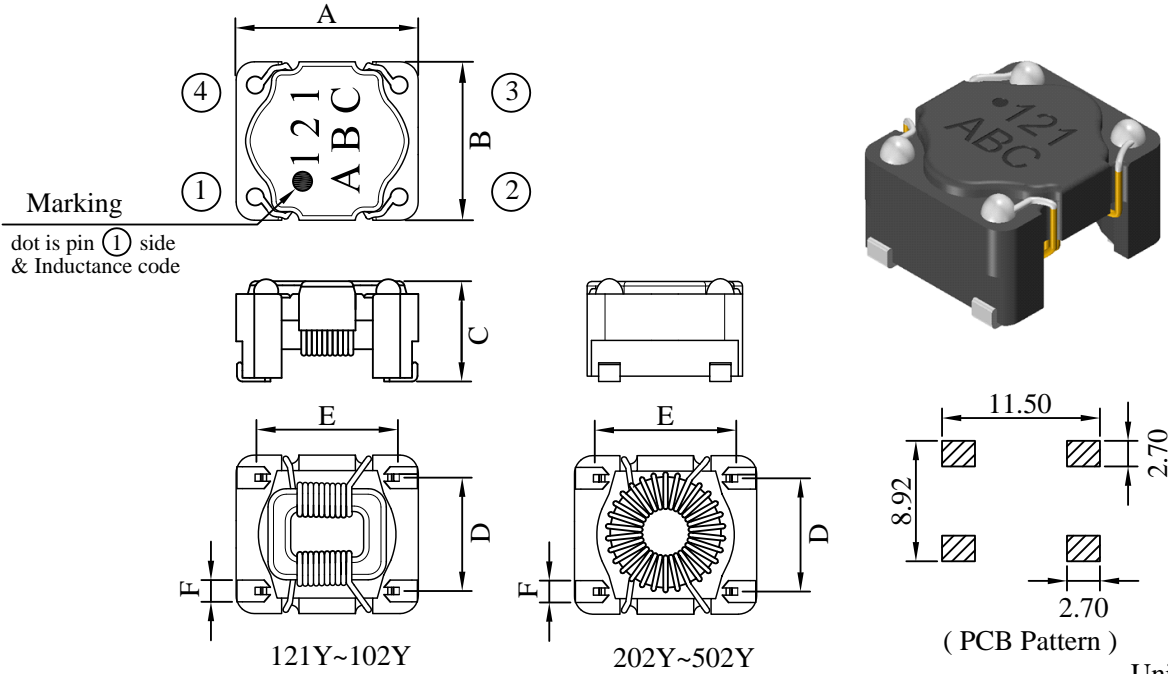


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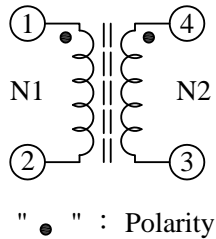
I . Configuration and dimensions :



Unit : m/m

A	B	C	D	E	F
10.00 ±0.5	8.70 ±0.3	6.50 max.	6.22 ±0.1	7.62 ±0.1	1.40 ref.

II . Schematic diagram :

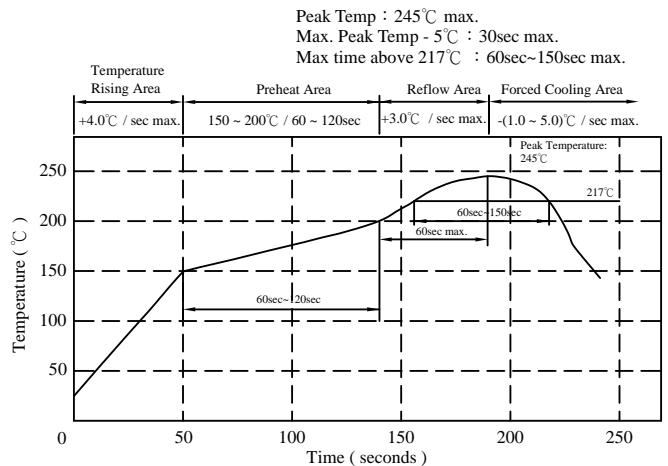


III . Description :

- a . Ferrite toroidal core construction.
- b . Enamelled copper wire : H class
- c . Product weight : 0.79g (ref.)
- d . Moisture sensitivity Level 1
- e . Products comply with RoHS' requirements
- f . Halogen free available

IV . General specification :

- a . Storage temp. : -25°C ---- +85°C
- b . Operating temp. : -20°C ---- +80°C
(Temp. rise included)
- c . Resistance to solder heat : 245°C. 10 secs.



SPECIFICATION FOR APPROVAL

REF. :

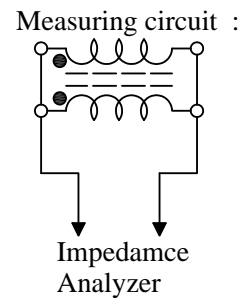
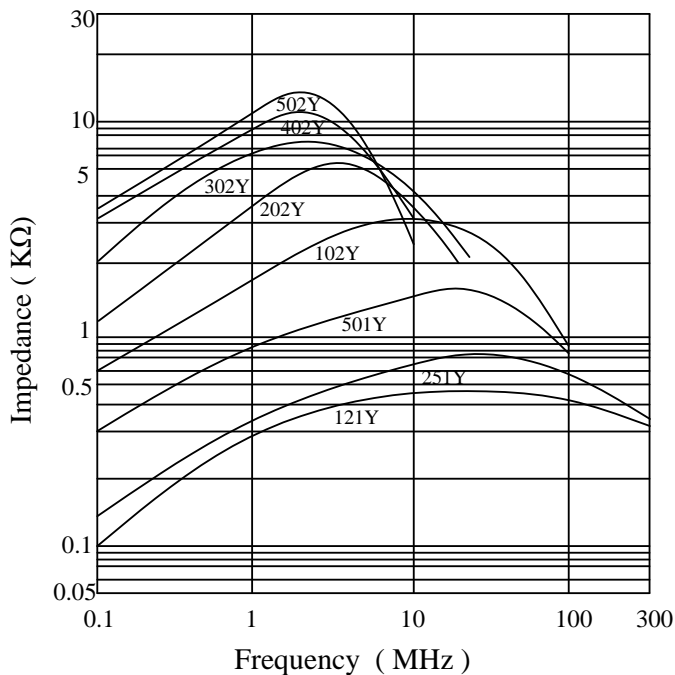
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V . Electrical characteristics :

DWG No.	Inductance L1 , L2 (μH)	RDC (Ω) max.	IDC (A) max.	Nominal voltage (V) Vdc	Impedance (Z)	
					Freq.range (MHz)	(Ω) min.
SF1065121YL□-□□□	120 ±40%	0.025	1.40	50	10 ~ 200	200
SF1065251YL□-□□□	250 ±40%	0.035	1.19	50	5 ~ 100	400
SF1065501YL□-□□□	500 ±40%	0.070	0.84	50	2 ~ 50	800
SF1065102YL□-□□□	1000 ±40%	0.180	0.52	50	1 ~ 40	1400
SF1065202YL□-□□□	2000 ±40%	0.270	0.40	50	0.5 ~ 15	2000
SF1065302YL□-□□□	3000 ±40%	0.330	0.35	50	0.5 ~ 10	3000
SF1065402YL□-□□□	4000 ±40%	0.550	0.30	50	0.5 ~ 5	4000
SF1065502YL□-□□□	5000 ±40%	0.620	0.25	50	0.5 ~ 3	5000

- 1). □ : Packaging information : □ Code
- 2). "-□□□" : Reference code
- 3). Electrical specifications at 25°C
- 4). Inductance Test Condition. : 100kHz / 0.1V
- 5). IDC base on Temp. rise 45°C max.
- 6). Hi-pot test : AC 1000V / 60Hz / 3mA / 1 min. (121Y ~ 102Y)
AC 300V / 60Hz / 3mA / 1 min. (202Y ~ 502Y)

VI . Curve :



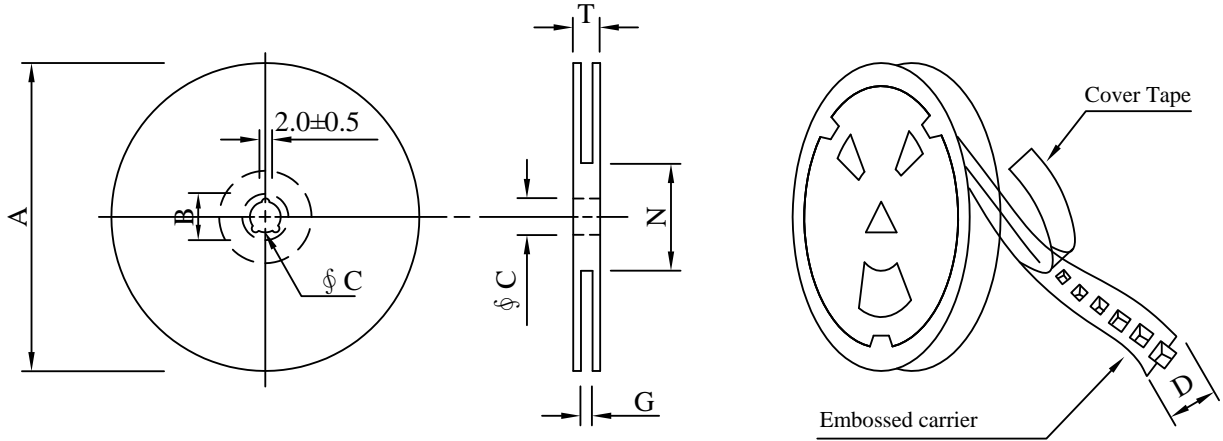
SPECIFICATION FOR APPROVAL

REF. :

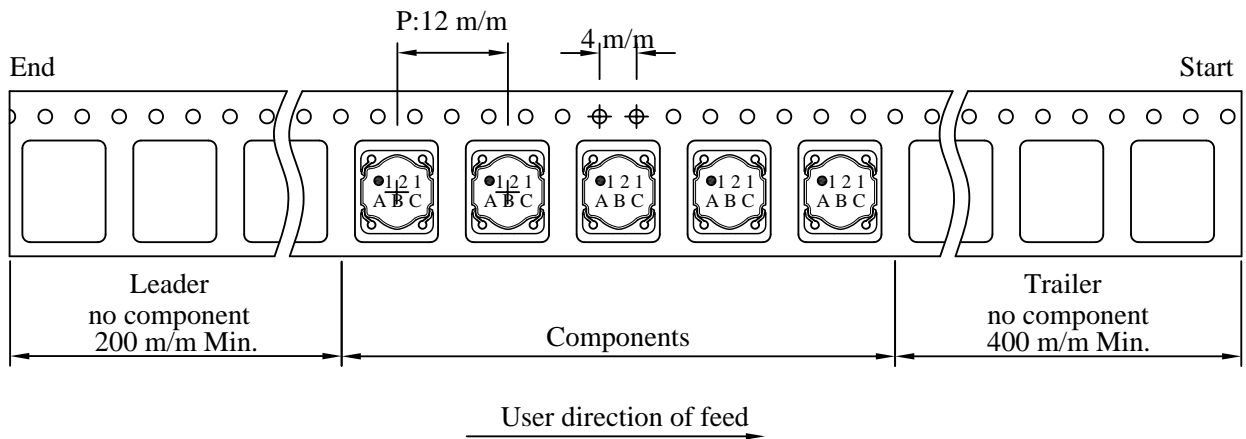
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VII . Packaging information :

(1) Configuration



※Carrier tape width : D



(2) Dimensions

Unit:m/m

Style	A	B	C	D	G	N	T
13 - 24	330	21±0.8	13±0.5	24	26 ⁺⁰	60 ⁻⁰	30.4

(3) Q'TY & G.W. Per package

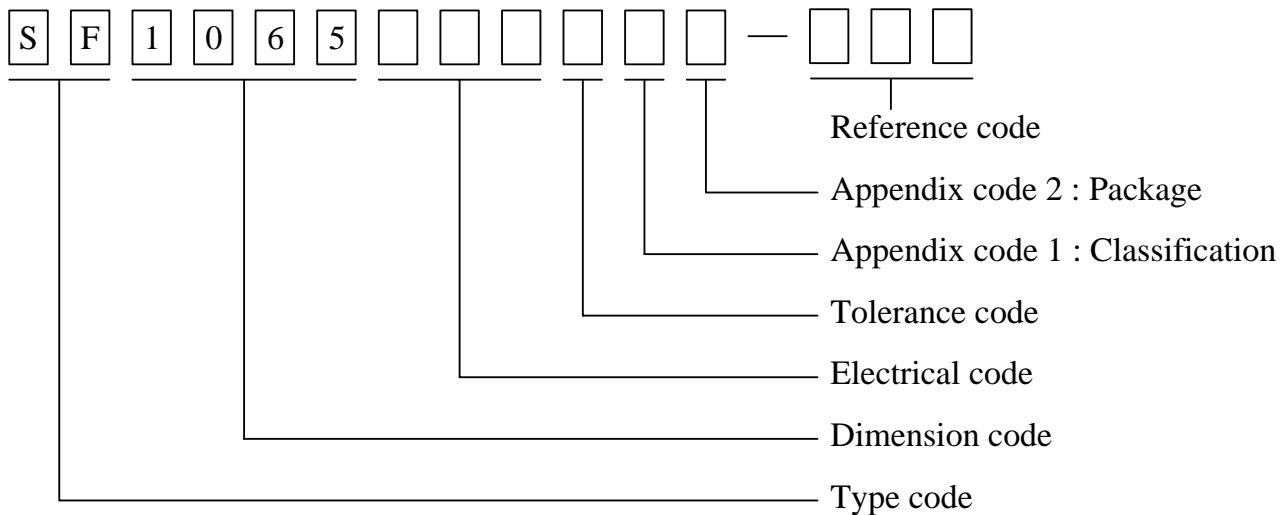
Code	Inner : Reel			Outer : Carton		
	Q'TY (pcs)	G.W. (gw)	Style	Q'TY (pcs)	G.W. (Kg)	Size (cm)
B	800	480	13 - 24	3,200	5.6	38 x 37 x 22

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VIII . Drawing number expression :



Appendix code 1 : Product Classification

Appendix code 2 : Package Information

Code	Inner package	Cover tape	Carrier tape	Bag	Package Q'TY	Remark
B	T /R (Reel package)	UCT	Antistatic	Antistatic	800 pcs	

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IX . Reliability test :

Item	Reference documents	Test Condition	Test Specification
1.High Temperature Exposure	MIL-STD-202 Method 108	1.Temperature: 85±2°C 2.Time:96±2 hours.	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±50%.
2.Temperature Cycling	JESD22-A 104	1.Temperature: -25°C ~ +85°C 2.Number of cycle:100 cycle 3.Dwell time:30 minutes	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±50%.
3.Biased Humidity Test	MIL-STD-202 Method 103	1.Temperature : 85±2 °C 2.Humidity: 85% RH. 3.Time:96±2 Hours	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±50%.
4.Operational Life	JESD22-A 108	1.Temperature: 80°C (Temp. rise included) 2.Time:96±2 hours. 3.Rated current :	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±50%.
5.External Visual	JESD22-B 101 & MIL-STD-883 Method 2009	Inspect product constructions, marking and workmanship.	1.No pollution on the surface of products. 2.Clear marking. 3.No crack.
6.Physical Dimensions	JESD22-B 100	Verify physical dimensions to the applicable product detail specification.	Per product specification standard
7.Resistance to solvents	MIL-STD-202 Method 215	Immerse into solvent for 3±0.5 minutes & brush 10 times for 3 cycles.	1.No body change in apperance. 2.No marking blurred. 3.Inductance shall not change more than ±50%.
8.Vibration Test	MIL-STD-202 Method 204	1.Frequency and Amplitiued : 10-2000-10 Hz, 1.5 mm. 2.Direction:X, Y, Z 3.Test duration:2 hours for each direction, 6 hours in total.	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±50%.
9.Resistance To Soldering Heat Test	MIL-STD-202 Method 210 & J-STD020D.1	1.Highest temperature : 245±5°C. 2.Time (temp. ≥ 217°C) : 60~150 Seconds. 3.IR reflow times : 3 times.	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±50%.
10.Over load	JIS C 6436 & User SPEC.	1.Applied one and half rated current for a period of 5 minutes. 2.Rated current :	No electrical or mechanical damage
11.Temperature Rise Current	JIS C 6436 & User SPEC.	1.Applied rated current for 10 minutes. 2.Temperature measure by digital surface thermometer. 3.Irms current :	Surface temperature rise is less than 45°C max.
12.Solderability Test	J-STD-002 & JESD22-B 102	1.Baking in pre-testing : 150±5°C / 16Hours±30 min. 2.Peak temperature : 240±5°C 3.Time (temp. ≥ 217°C) : 60~150 seconds. 4.IR reflow times : 1 time.	More than 95% soldering coverage min on terminations.
13.Electrical Characteriazation	MIL-STD-202 Method 304 & User SPEC.	1.Operating temperature : -20°C~80°C 2.Room temperature : 25°C.	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±50%.
14.Withstanding Voltage Test	MIL-STD-202 Method 301 & User SPEC.	1.AC: 1000V (Winding to Winding) 2.Time : 1minutes	1.During the test no breakdown. 2.No mechanical or electrical damage.
15.Drop	CNS-C6354 & GB/T 2423.8	1.Products shall be mounted on SPEC. pcb and dropped down from a heigh of 1m 2.Drop total time : 6 times (Every side ofsample drop 2 times)	1. Adhesion on PCB shall be enough. 2. Product appearance shall not break. 3. No electrical damage.
16.Terminal Strength Test	IEC 60068-2-21	1.Apply push force to samples mounted on PCB. 2.Force of 1.8 kg for 60±1 seconds.	After test, inductors shall be no mechanical damage.

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X . Change history :

DATE/REV.	DISCRIPTION	DRAWN	CHECKED	APPROVED
20141216-A	Released	Leo Liang	Nick Chen	Nick Chen
20150821-B	Modify the Reliability test	Leo Liang	Nick Chen	Nick Chen
20160908-C	Add Change history and Dwging number expression	Leo Liang	Nick Chen	Nick Chen

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